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PTO/SB/21 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

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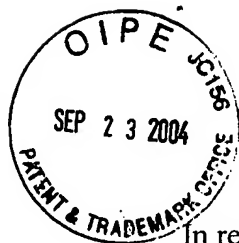
TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/805,822
	Filing Date	03/22/2004
	First Named Inventor	Makoto Yamamoto et al.
	Group Art Unit	2815
	Examiner Name	Tom Thomas
Total Number of Pages in This Submission	Attorney Docket Number	44471/298721

ENCLOSURES (check all that apply)		
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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Brenda O. Holmes, Reg. No. 40,339 KILPATRICK STOCKTON LLP
Signature	<i>Brenda O. Holmes</i>
Date	09.21.2004

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Makoto Yamamoto et al.

Serial No. **10/805,822**

(Divisional of 10/014,949 filed October 26, 2001)

Filed: **March 22, 2004**

For: **Method of Fabricating Semiconductor
Integrated Circuit**

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) **Art Unit: 2811**
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) **Examiner: Tom Thomas**
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) **Attorney Docket No.: 44471/298721**

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Janie Wilkins

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Sir:

The citation of information on the attached Forms PTO/SB/08A and B, "List of Art Cited by Applicant" is made pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98. A copy of each non-U.S. patent document listed is enclosed.

Each item of information contained in the attached Forms PTO/SB/08A and B was first cited in the European Search Report mailed on August 23, 2004 in a counterpart European application. We also enclose a copy of the European Search Report.

This Supplemental Information Disclosure Statement is being filed before the mailing of a first Office Action on the merits and each item of information contained in the accompanying Forms PTO/SB/08A and B was first cited in communication in a counterpart foreign application not more than three months prior to the filing of the statement. Therefore, no fee is required.

The citation of this information does not constitute an admission of priority or that any cited item is available as a reference, or a waiver of any right the applicant may have under applicable statutes, Rules of Practice in patent cases, or otherwise.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Brenda O. Holmes", with a stylized, flowing script.

Brenda O. Holmes
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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known

Application Number	10/805,822
Filing Date	March 22, 2004
First Named Inventor	Makoto YAMAMOTO
Art Unit	2815
Examiner Name	Tom Thomas
Attorney Docket Number	44471/298721

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
	1	US- 4,110,126	8-29-1978	Bergeron et al.	
	2	US- 5,326,710	7-5-1994	Joyce et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)				
	3	EP 0 435 541 A	7-3-1991	Motorola Inc.		

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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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**INFORMATION DISCLOSURE
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Sheet 2 of 2

Complete if Known

Application Number	10/805,822
Filing Date	March 22, 2004
First Named Inventor	Makoto YAMAMOTO
Art Unit	2815
Examiner Name	Tom Thomas
Attorney Docket Number	44471/298721

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	4	MICHAEL P. MASQUELIER and DAVID N. OKADA, Institute of Electrical and Electronics Engineers, "Integration of a 200V, 60MHz Lateral PNP Transistor with Emitter-Base Self-Aligned to Polysilicon, into a High Voltage BiCMOS Process", Proceedings of the 3 rd International Symposium on Power Semiconductor Devices and ICs, pp. 56-60, XP000218972, 1991 IEEE, April 22-24, 1991, Baltimore, Maryland, USA	

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